

TimeLine-Alesis

Improved AI-2 Service Manual

reformatted from the original by Bill Bainbridge (brainditch)
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Table Of Contents

1. Introduction.....	2
2. Power Supply Test.....	3
3. Front Panel Keyboard, Configuration Switch, LCD Display Tests.....	4
4. Counter IC And Serial COM 1-6 Tests.....	6
5. Time Code Out Test.....	8
6. Sample Clock Generation Test.....	9
7. VSG Interface Test.....	10
8. Video Sync Separator Test.....	11
9. Time Code Reader Test.....	12
10. Word Clock In Test.....	13
11. Battery Test.....	14
Bills of Material.....	18
AI-2 Main PCB Schematic.....	separate document
Addendum.....	22

List of Figures

Figure 1. AI-2 PCB Assembly Drawing.....	15
Figure 2. Remove the Top Cover.....	16
Figure 3. Sync Out Loopback Connector Schematic.....	16
Figure 4. Editor Loopback Connector Schematic.....	16
Figure 5. Lynx Loopback Connector Schematic.....	17
Figure 6. Timecode Sample Definition.....	17

1. Introduction

This service manual provides instructions to allow service personnel to verify that an AI-2 is operating correctly, isolate a problem to a user replaceable part, or determine if factory service is required. The tests should be performed by an advanced electronics technician.

Warning

There are static sensitive IC's on the main circuit board, The unit should only be serviced on a static safe workstation. Standard ESD practices must be adhered to.

Some tests can be performed at the front panel alone, while most require removal of the top cover. Some communication/data tests require loop back cables; see the list of figures for cable schematics.

The table of contents lists the areas of the AI-2 currently tested. When troubleshooting, if the problem is understood, service personnel can proceed directly to the relevant section. To verify the overall integrity of the AI-2, all of the tests should be performed in the order given.

Required Parts:

Software:

One PROM with the released AI-2 program (which includes test programs)

Hardware:

Video sync sources (at least one): black burst, comp sync, color bars (NTSC, PAL)

Oscilloscope with frequency counter, or Frequency counter

Digital Multimeter

Squarewave Generator, 100Hz-125kHz

Longitudinal Time Code Generator

One 70A071 10V AC power supply (As supplied with the AI-2)

One 70C025 VSG board (for VSG interface test only)

One DB 9 Male Connector wired per Figure 3

One DB 9 Male Connector wired per Figure 4

One DB 25 Male Connector wire per Figure 5

Two 5-pin DIN male cables, wired straight across

2. Power Supply Test

Turn off the AI-2 and remove the top cover (see Figure 2). See Figure 1, AI-2 Assembly Drawing, for component and test point location. To find the relevant circuit areas, see the schematic sheet 2, zone A1 thru A4. Note: The grayed areas of the schematic do not apply to the AI-2.

A. Connect a 9-12V AC output power transformer to the AI-2 POWER connector.

B Turn the POWER switch to ON.

C. Set Ch.1 of oscilloscope to 5V/div. Measure the +12V_UN signal at E9. Verify that the signal looks like a full wave rectified sinewave with a frequency of twice the AC mains frequency (either 100Hz or 120 Hz) and a peak of +11.0V +/- 0.5V decaying to +9.0+/-0.5V min.

If test fails, it may not be a power supply failure, but could be a malfunctioning IC anywhere on the main PCB, that has failed and shorted. Check if any IC is very hot. Also check rectifying diodes D10-D13.

D. Measure the -VEE signal a E8. Verify that the signal is -7.5V to -12V.

If fails, check U32.

E. Verify that the voltage at +5V, E2, is +4.85V to +5.15V.

If fails, check U16.

F. Verify that the voltage at -5V, E7, is -4.85V to -5.15V.

If fails, check U14. See schematic sheet 3, zone A2.

G. Verify that the voltage at +5VR, E6 is +4.85V to +5.15V.

If fails, regulator or other IC in the Sample Clock "can" area is bad. Board has to be replaced. See schematic sheet 2, zone C4.

3. Front Panel Keyboard, Configuration Switch, LCD Display Tests

These Tests can be performed by pressing the indicated front panel keys; the top panel need not be removed.

(User's Commands Summary):

SETUP then POWER = Diagnostics tests

SETUP = start test

DISPLAY = next test

- A. Turn off the AI-2. Press the SETUP switch and the power switch, simultaneously, to turn on the AI-2. The display should scroll through the following messages:

TIMELINE AI-2, DIAGNOSTICS

Power on test, in progress

Diagnostics Mode, Menu to select

FP SWITCH TEST, SETUP to start

- B. Verify that the Power On test (checks RAM, U25) passed.

If test fails, check that the software (U18) is installed correctly. Otherwise, one of the IC's in the processor section has probably failed. Return the unit for factory service. See the schematic sheet 1, zones B2 thru D4.

- C. Press the SETUP switch to start the FP SWITCH TEST. Follow the instructions on the display, and verify that the test passes.

If test fails, check that the Keyboard cable is plugged in correctly to J20. See the schematic sheet 4, zone B1.

- D. Press the DISPLAY switch; the display should show the following:
CONFIG SW TEST, SETUP to start.

Press the SETUP switch to start the CONFIG SWITCH TEST. The test tells the service personnel, on the display, to set the AI-2 dip switch three different ways. Follow the instructions, and verify that the test passes.

If test fails, check switch S1. See the schematic sheet 1, zone B1.

- E. Press the DISPLAY switch: the display should show the following:
FP LED TEST, SETUP to start.

Press the SETUP switch (twice) to perform the FP LED TEST. Follow the instructions, and verify that the test passes. The LED's should show the following pattern:

00000000, 01010101, 11111111, 00000000. (1=on, 0=off.)

If test fails, check diodes D16-D23. See the schematic sheet 4, zone B1.

F. Press the DISPLAY switch; the display should show the following:

LCD DSPL TEST, SETUP to start.

Press the SETUP switch to start the FP LCD TEST. Verify that A-Z, a-z, 0-9, and special control characters are successfully displayed. Also verify that all the pixels are dark in the last display test pattern.

If test fails, check that the Display cable is plugged in correctly to J19. See the schematic sheet 4, zone C1.

4. Counter IC And Serial COM 1-6 Tests

These tests require taking the top cover off of the AI-2 (see Figure 2), and placing a jumper from a debug header to ground, and cycling power to enter a special test software mode of operation.

(Serial Test Keyboard Commands Summary):

SETUP = start test

MENU = next test

SET/HOLD = previous test

- A. Turn off the AI-2, and take off the top cover. Place a jumper from DH1 pin 8 to a ground test point, E10. (Square dot indicates pin 1). Turn on the AI-2. Verify that the following screens scroll through the display:
TIMELINE AI-2, ACCEPTANCE TEST
Power on test, in progress
Diagnostic Mode, Menu to select
COUNTER RD TST, SETUP to start.
- B. Press the SETUP switch to start the COUNTER RD TST. Verify that the test passes. If test fails, check U22. (Surface mount part, may need to be returned to factory for service). See the schematic sheet 2, zone B1, U22.
- C. Attach a MIDI cable from J5 (BRC MIDI OUT) to J6 (BRC MIDI IN). Attach the DB 9 Male loopback connector, wired per Figure 3, to J1 (SYNC OUT).
- D. Press the DISPLAY switch; the display should show the following:
SERIAL COM TST1, SETUP to start.
Signals tested: J1: SRL_O+/-, SRL_IN+/-, MIDI_TX1, MIDI_IN_SYNCO.
- E. Press the SETUP switch to start the SERIAL COM TST1. Verify that the test passes. If test fails, replace U6 or U2A. See the schematic sheet 2, zone C2 & D2, J1.
- F. Press the DISPLAY switch; the display should show the following:
SERIAL COM TST2, SETUP to start.
Signals tested: J5: MIDI OUT BRC, MIDI_TX1-, J6: MIDI_IN_BRC.
- G. Attach/leave the MIDI cable from J5 (BRC MIDI OUT) to J6 (BRC MIDI IN).
- H. Press the SETUP switch to start SERIAL COM TST2. Verify that the test passes.
- I. Press the DISPLAY switch; the display should show the following:

SERIAL COM TST3, SETUP to start.
Signals tested J5: MIDI OUT BRC, MIDI_TX1-, J6: MIDI_IN_BC, J4: MIDI_THRU BRC, MIDI_THRU-, J9: MTC MERGE IN, MIDI_RX2.

- J. Attach/leave the MIDI cable from J5 (BRC MIDI OUT) to J6 (BRC MIDI IN), and attach a cable from J9 (CONTROL MIDI IN) to J4 (BRC MIDI THRU).
- K. Press the SETUP switch to start SERIAL COM TST3. Verify that the test passes. If test fails, replace U9A. See the schematic sheet 3, zone B3 & C3.
- L. Press the DISPLAY switch; the display should show the following:
- SERIAL COM TST4, SETUP to start.
Move the MIDI cable from J4 (BRC MIDI THRU) to J8 (CONTORL MIDI OUT).
Signals tested: J8: MTC OUT, MIDI_TX2-, J9: MTC MERGE IN, MIDI_RX2.
- M. Press the SETUP switch to start SERIAL COM TST4. Verify that the test passes. If test fails, replace U6. See the schematic sheet 3, zone B4, C4 & D4.
- N. Press the DISPLAY switch; the display should show the following:
- SERIAL COM TST5, SETUP to start.
Signals tested: J10: ETX+/-, LRX+/-.
- O. Attach the DB 25 Male loopback connector, wired per Figure 5, to J10 (LYNX/MICRO LYNX).
- P. Press the SETUP switch to start the SERIAL COM TST5. Verify that the test passes. If test fails, replace U10 or U11. See the schematic sheet 1, zone B1 & C1.
- Q. Press the DISPLAY switch; the display should show the following:
- SERIAL COM TST6, SETUP to start.
Signals tested: J11: RS422_TX+/-, RS422_RX+/-.
- R. Attach the DB 9 Male loopback connector, wired per Figure 4, to J11 (EDITOR).
- S. Press the SETUP switch to start the SERIAL COM TST6. Verify that the test passes. If test fails, replace U10 or U11. See the schematic sheet 1, zone B4.

5. Time Code Out Test

A. Press the DISPLAY switch; the display should show the following:

TC OUT TEST, SETUP to start.
Signals tested: J3: GCO, J10 GTO+/-.

B. Press the SETUP switch. Verify that the display indicates:
TC OUT TEST, Test in progress.

C. Connect an oscilloscope to a phone plug inserted in J3, scope ground to ring or sleeve, tip to scope input. Verify that the oscilloscope shows a 2.06kHz \pm 100Hz, slightly rounded square wave at 1.0Vpp \pm 0.1Vpp.
If test fails, check U12. See the schematic sheet 3, zone C2.

D. Move the oscilloscope to J10 pins 6 and 7. Verify, on both pins, that the oscilloscope shows a 2.06kHz, 0.29Vpp to 0.32Vpp squarewave.
If test fails, replace U10. See the schematic sheet 1, zone C1.

G. Press the STORE switch to end the test.

6. Sample Clock Generation Test

Signal tested: J2: 48kHz OUT

- A. Press the DISPLAY switch; the display should show the following:
PLL CKT TEST, SETUP to start.
- B. Press the SETUP switch to start the PLL CKT TEST; the display should show the following:
PLL CKT TEST, Test in Progress.
PLL CKT TEST, 48K/24 VCO nom.
- C. Connect an oscilloscope (or frequency counter) to J2, W/C OUT. Verify that the frequency is 48.056kHz \pm 5Hz, 0V to 3.5V. The jitter of the frequency should be \pm 0.2Hz. If signal amplitude is missing/wrong, replace U34. See the schematic sheet 3, zone D2. If frequency is wrong, check crystal Y1. If Y1 is OK, return board for factory service. See the schematic sheet 1, zone D1. See also schematic sheet 2, zones B3, C3, B4 and C4. (The circuit in the "can" area is not shown).

The above suggested repairs apply to the next four steps.

- D. Press the STORE switch to go to the next test; the display should show the following:
PLL CKT TEST, 48K/30 VCO NOM.
Verify that the oscilloscope indicates a frequency of 48.074kHz \pm 5Hz. The jitter of the frequency should be \pm 0.2Hz.
- E. Press the STORE switch to go to the next test; the display should show the following:
PLL CKT TEST, 38K/30 VCO LOW.
Verify that the oscilloscope indicates a frequency of 38.008kHz \pm 5Hz. The jitter of the frequency should be \pm 0.2Hz.
- F. Press the STORE switch to go to the next test; the display should show the following:

PLL CKT TEST, 58K/30 VCO HIGH.
Verify that the oscilloscope indicates a frequency of 58.013kHz \pm Hz.
- G. Press the STORE switch to go to the next test; the display should show:
PLL CKT TEST, PLL LOCK Test. Press the SETUP switch; verify that the test passes.
- H. Press the STORE switch to get out of the test.

7. VSG Interface Test

Note, you can only do this test if you have a VSG card, TimeLine Part Number 70C025. It also helps to have an oscilloscope with TV field trigger capability.

A. Press the DISPLAY switch; the display should show the following:

VSG INTF TEST, SETUP to start.

Connect a coax cable from the VID IN connector to an oscilloscope channel. Set the channel to 0.2V/div, and trigger on TV lines, or fields, if possible. Set the sweep time to 1mS/div.

B. Press the SETUP switch to start the VSG INTF TEST; the display should show the following:

VSG INTF TEST, Video ON test.

Verify that the oscilloscope shows a 0.58Vpp TV sync signal. Wait until front panel LED D16 (ON-LINE) lights (approximately three seconds). If signal amplitude is missing/wrong, check the VSG card. See the schematic sheet 3, zone A4.

C. Press the STORE switch; the display should show the following:

VSG INTF TEST, Video OFF test.

Verify that the oscilloscope shows no TV signal.

D. Press the STORE switch to end the test. Verify that the display indicates the test was passed, and LED D16 (ONLINE) goes off.

8. Video Sync Separator Test

- A. Press the DISPLAY switch 3 times (skip the Miscellaneous Test 1 & 2, for factory use only); the display should show the following:

VID FLD TEST, SETUP to start.

- B. Attach a coax cable and terminator(s) per the following table. Press the SETUP switch to perform the test. Verify each table entry:

<u>SYNC TYPE</u>	<u>75 OHM TERMINATION</u>	<u>PASS/FAIL</u>
C SYNC	NONE	_____
C SYNC	ONE TERMINATION	_____
C SYNC	TWO TERMINATIONS	_____
COL BARS	ONE TERMINATION	_____
COL BARS	TWO TERMINATIONS	_____
BLK BURST	NONE	_____
BLK BURST	ONE TERMINATION	_____
BLK BURST	TWO TERMINATIONS	_____
PAL	NONE	_____
PAL	ONE TERMINATION	_____
PAL	TWO TERMINATIONS	_____

If test fails, check U12 and U15. See the schematic sheet 3, zone A3 & A2.

9. Time Code Reader Test

- A. Turn off the AI-2 and, if necessary, disconnect the jumper installed in Section 4, Step A, going from the DH1 pin 8 to ground. Turn the power on. Verify that the display shows "CLEARING MEM", then scrolls through the current (default), user settings and the unit is ready to accept commands.
- B. Connect a squarewave generator to an oscilloscope channel and to AI-2 connector RDR IN, (signal to tip, ground to sleeve). Set the generator output to be a 200mVpp, 1kHz squarewave. Also display this signal and use it as the trigger source on the oscilloscope.
- C. Attach a scope lead to AI-2 E4 (SMPL).
- D. Sweep the generator frequency from 100Hz to 125kHz; verify that the SMPL tracks the generator output. The SMPL signals rising edge should be at approximately the three-quarters point of a "bit". (A "bit" can either be one whole cycle, or one-half cycle of the squarewave generator output. See Figure 6).
If test fails, check U12D, U24 and U34. See the schematic sheet 4, zone D3 & D4.
- E. Set the generator to 1kHz. Turn the generator output level to 8Vpp. Verify that the SMPL signal rising edge still occurs at the three-quarters point of a bit as defined above.
- F. Connect the time code source, time code output to the AI-2 J14 RDR IN connector. Set the output time code to play speed.
- G. Press the DISPLAY switch three times until the screen reads:

Reader Input
v 00:00:00:00 (or any time code value)
- H. Verify that the display is showing time code incrementing smoothly.

10. Word Clock In Test

- A. Set the squarewave generator output to be 48000Hz \pm 100Hz, 0V to 5V. Attach the squarewave generator to the W/C IN connector, J13.
- B. Turn off the AI-2, hold the STORE switch, turn the power on, and let go of the STORE switch. The display should indicate that the memory was cleared and will scroll through the default user settings; the unit is ready to accept commands.
- C. Press the SETUP switch once, the display should show:
Setup: REFERENCE
Input: W/C, Int
- D. Verify the REF LED on the AI-2 front panel becomes steady on. This indicates that the AI-2 has locked to the input 48kHz signal.
If test fails, replace U6. See the schematic sheet 3, zone B2.

11. Battery Test

A. Turn off the AI-2, hold the STORE switch, turn the power on, and let go of the STORE switch. The display should indicate that the memory was cleared and will scroll through the default user settings; the unit is ready to accept commands.

B. Press the SETUP switch, the display should show:

Setup Adat:
All Input: Off

C. Press the DISPLAY switch two times, the display should show:

Setup: CONTROL
Mode: AUTO

Then press the UP switch to:
Setup: CONTROL
Mode: LYNX

D. Turn off the AI-2, wait one minute, then turn it on. During the usual display startup messages, verify that CONTROL is still LYNX.

E. Turn off the AI-2, hold the STORE switch, turn the power on, and let go of the STORE switch. After the display indicates that the memory was cleared, turn the unit off.

G. Use a DMM to measure the battery BT1 voltage. It should be between 2.5V and 3.7V. If test fails, replace the battery. Replace cover, end of test.

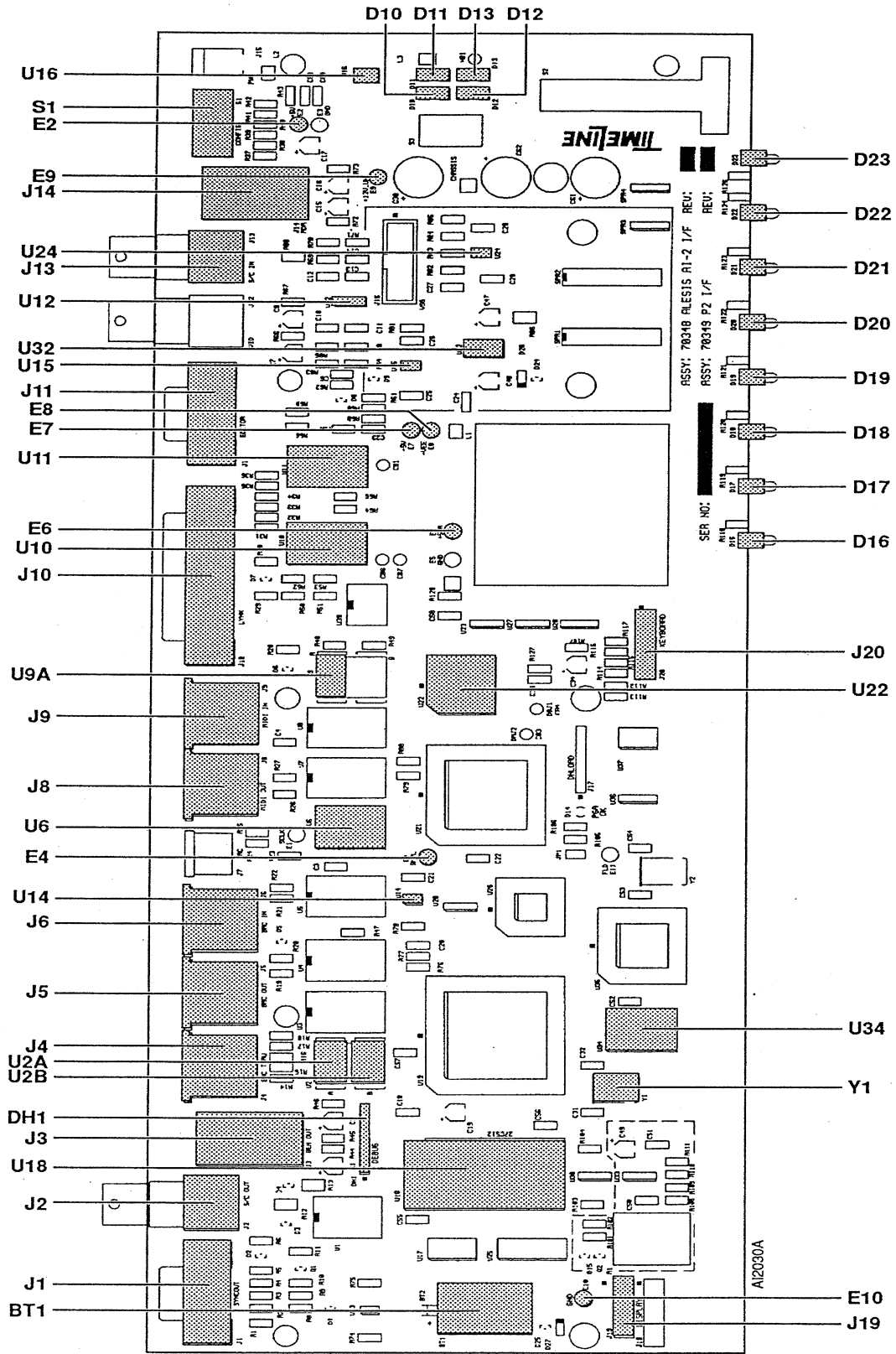


Figure 1. AI-2 PCB Assembly Drawing

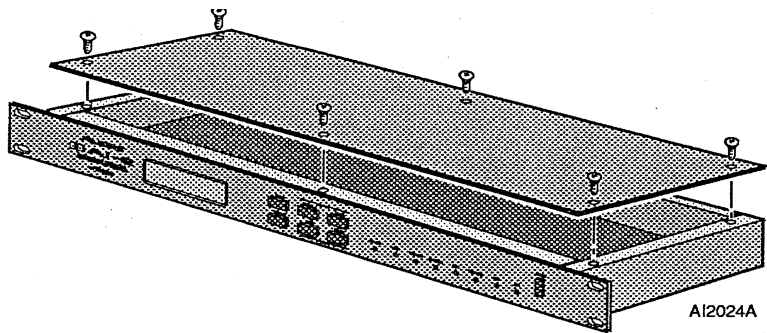


Figure 2. Remove the Top Cover

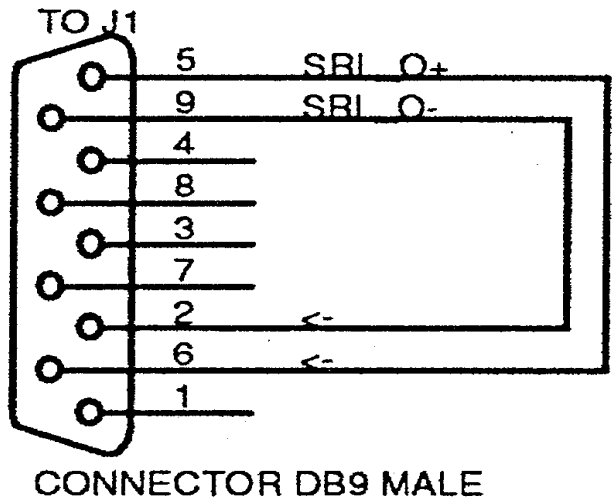


Figure 3. Sync Out Loopback Connector Schematic

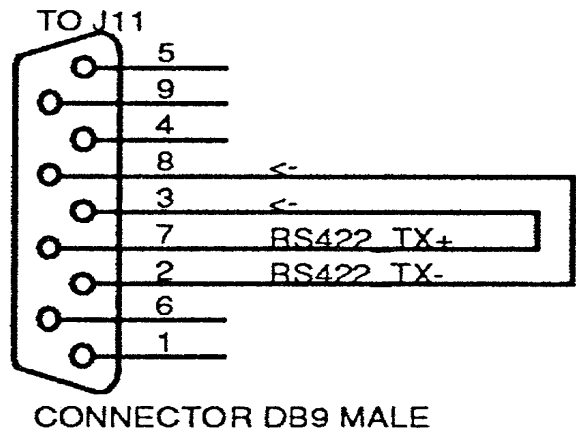


Figure 4. Editor Loopback Connector Schematic

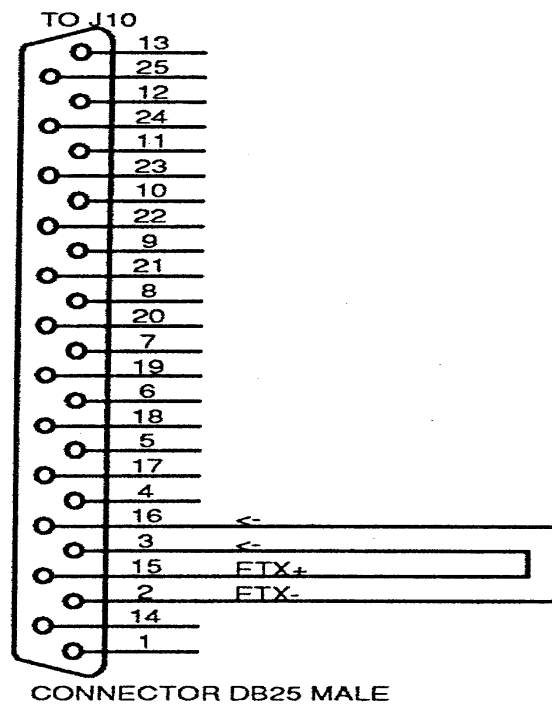
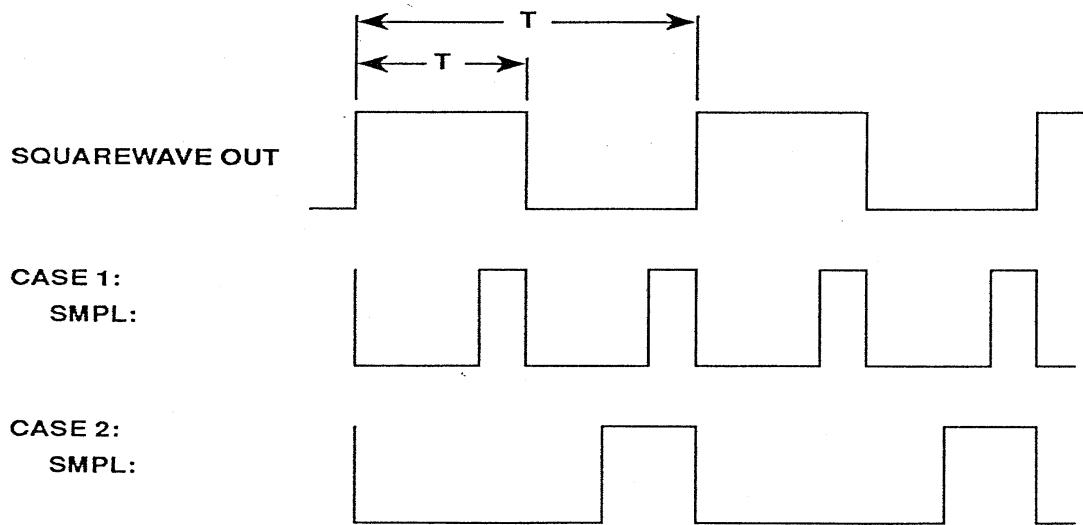


Figure 5. Lynx Loopback Connector Schematic



A12029A

EITHER SMPL CASE IS ACCEPTABLE.
SMPL CAN BE LOCKED ON EITHER FALLING OR RISING EDGE OF SQUAREWAVE OUT.

Figure 6. Time Code Sample Definition

Bills of Material

Part Number 70C052
ASSY PCB AI-2 I/F
Revision Level: A8

Part Number	Description	Pkg. Type	Quantity	
55A053-C	PCB ETCHED, AI-2 MAIN AND KBD BEFORE BKWY		1	EA
20239	RES MF SM 330R 1/8W 5% R44, R54, R106, R118, R119, R120, R121, R122, R123, R124, R125	1206	11	EA
22206	SEMI RECT SM 1A 50V PIV D10, D11, D12, D13		4	EA
25507	IC INTFC 2691 UART U26	PLCC28	1	EA
25509	IC SF 82C54 COUNTER U22	PLCC28	1	EA
30120	SOCKET IC DIP, 14 PIN, SM U2, U6, U9, U34	ICS14	4	EA
30121	SOCKET IC DIP, 16 PIN, SM U7, U8, U10, U11	ICS16	4	EA
30124	SOCKET IC DIP, 32 PIN, SM U18	ICS32	1	EA
20202	RES MF SM 10K 1/8W 5% R30, R31, R32, R34, R35, R38, R39, R40, R41, R42, R43, R51, R53, R56, R58, R59, R61, R69, R71, R72, R73, R77, R104, R105, R110, R111, R112, R113, R114, R115, R116, R117, [R116] (LISTED TWICE)	1206	33	EA
20205	RES MF SM 1K 1/8W 5% R21, R22, R48, R50, R52, R55, R66, R67, R68, R85, R90	1206	11	EA
20210	RES MF SM 220R 1/8W 5% R2, R3, R5, R8, R10, R14, R15, R16, R19, R20, R23, R26, R27 R28, R33, R36, R47	1206	18	EA
20211	RES MF SM 47K 1/8W 5% R57	1206	1	EA
20213	RES MF SM 470K 1/8W 5% R82	1206	1	EA
20222	RES MF SM 33K 1/8W 5% R96	1206	1	EA
20214	RES MF SM 680K 1/8W 5% R81	1206	1	EA
20216	RES MF SM 560R 1/8W 5% R62	1206	1	EA
20218	RES MF SM 1.2K 1/8W 5% R64, R65	1206	2	EA

Part Number	Description	Pkg. Type	Quantity	
20221	RES MF SM 200K 1/8W 5% R83	1206	1	EA
20226	RES MF SM 27R 1/4W 5% R13, R86	1210	2	EA
20230	RES MF SM 3K 1/8W 5% R70	1206	1	EA
20233	RES MF SM 0R 1/8W R37, R45, R84, R108	1206	4	EA
20235	RES MF SM 300K 1/8W 5% R96, R100	1206	2	EA
20232	RES MF SM 91K 1/8W 5% R76	1206	1	EA
20304	RES PREC SM 348R 1/8W 1% R89	1206	1	EA
21A014	CAP ELECT 1000uF 25V RADIAL C30, C61 NOTE: ONLY USE AFTER USE UP OF 21A009 (QTY OF 3) AT REFERENCE DESIGNATIONS; C30, C61 & C62		2	EA
21C008	CAP POLY 0.1uF 50V RADIAL C44		1	EA
21C008	CAP POLY 1.0uF 50V RADIAL C46		1	EA
21101	CAP ELECT SM 10uF 35V SM C1, C7, C9, C15, C16, C17, C19, C36, C47, C48, C49	EC5	11	EA
21402	CAP CER SM 0.1uF 50v R63	1206	1	EA
21403	CAP CER SM 0.001uF 100V NPO C23	1206	1	EA
21405	CAP CER SM 22pF 50V C12, C14, C31, C32, C53, C54	1206	6	EA
21408	CAP CER SM 100pF 100V C5, C43	1206	2	EA
21413	CAP CER SM 330pF 50V NPO C8, C10	1206	2	EA
22101	SEMI DIODE SM 1N914 D1, D3, D4, D5, D6, D6, D7, D8, D9	SOT23	8	EA
23A017	OPTO LED ROUND T1, GRN D14		1	EA
23A017	OPTO LED ROUND T1, RED, RIGHT-ANGLE D18		1	EA
23A014	OPTO LED ROUND T1, GRN, RIGHT-ANGLE D16, D17, D19, D20, D21, D22, D23		7	EA

Part Number	Description	Pkg. Type	Quantity	
25207	IC 74HC74 FF D DUAL U23	SO14	1	EA
25201	IC 74HC4046A PLL – MOTOROLA NOTE: MOTOROLA ONLY, NO SUBSTITUTES U31	SO16	1	EA
25202	IC 74HC573 LATCH OCTAL NINV U17	SO20	1	EA
25211	IC 74HC08 AND 2 INPUT QUAD U38	SO14	1	EA
25402	IC RAM 62256 32K STATIC U25	SOL28	1	EA
25801	IC MICRO 80C537 PROC CMOS U19	PLCC84	1	EA
25601	IC LIN TL074 OPAMP BIMOS U12, U33	SO14	2	EA
25602	IC LIN LM311 COMP U24	SO8	1	EA
25701	IC REG 79L05 -5V U14	SO8	1	EA
25802	IC SF XILINX XC3020 U21	PLCC68	1	EA
25803	IC SF BATTERY CTRL, BQ2201 U13	SO8	1	EA
24714	IC LIN LM1881M VID SYNC SEP U15	SO8	1	EA
24810	IC REG SI7661 VOLTAGE INVERTER U32	DIP8	1	EA
31413	CONN D PCB, 25 PIN FGM RT ANG W/SCR LOCK J10		1	EA
48404	CONN PWR, 2.5MM J15		1	EA
50230	SW PB PCB 2PDT RT ANGLE S3		1	EA
50902	SW DIP 8 POS RT ANGLE S1		1	EA
22A002	SEMI DIODE 1N914 D26, D27		2	EA
23D003	OPTO ISOL HI-SPD ACTV H11L1 U2A, U9A, U2B	DIP6	3	EA
24J009	IC CD 4031 S/R 64 BIT U7, U8	DIP16	2	EA
24N021	IC INTFC 26LS32 RS-422 RECEIVER U11	DIP16	1	EA

Part Number	Description	Pkg. Type	Quantity	
24NO23	IC INTFC 26LS31 RS-422 LINE DRIVER U10		1	EA
24R004	IC REG 780-05 +5V PREC U16		1	EA
24R007	IC REG LM317 VOLT REG ADJUSTABLE AV# LM317LZ MFG. NATIONAL U30	TO92	1	EA
31D010	CONN D PCB 9 PIN FEM RT ANG W/SCR LOCK J1, J11		2	EA
32A001	CONN 0.100 HDR 1 ROW 2 PIN DH1: INSTALL CONNECTOR IN PIN 7 AND 8.		1	EA
32B004	CONN 0.100 HDR 2ROW 16 PIN J20		1	EA
32B018	CONN 0.100 HDR 2ROW 16 PIN SHR, 0.6"H SLIM J16		1	EA
32B022	CONN 0.100 HDR 2ROW 14 PIN J19		1	EA
49A002	CONN MISC DIN-5, FEMALE, PCB R/A, LO-PRO J4, J5, J6, J8, J9		1	EA
49B006	CONN MISC BNC, PDB RT-ANG, METAL J2, J12, J13		3	EA
49E002	CONN MISC PHONE JACK, PCB, R/A LO-PROFIL J3, J14		2	EA
49H002	CONN MISC, TP, LOOP E1, E3, E4, E5, E10		5	EA
52E001	TRAN COIL BEAD FERRITE L1, L2, L3 (PREVIOUSLY UNLISTED)	2PTH	3	EA
59B002	ELECT MISC XTAL 12.0000 MHz Y1		1	EA
59B004	ELCT MISC XTAL 3.6864 MHz Y2		1	EA
59F003	ELECT MISC BATT LITH 3.6V, 1/2AA BT1		1	EA
70D120	ASSY CBL RIB FEM-16 TO TRANS-16, 4" J20 TO ASSY 55152 FP SW DAUGHTER PCB		1	EA

PREVIOUSLY UNLISTED:

	ASSY CBL RIB FEM-14 TO FEM-14 J19 TO P19-141 DISPLAY LCD		1	EA
55152	ASSY PCB FP 6-SW MOUNTED TO FRONT PANEL (FP)		1	EA

P19-141	ASSY DISPLAY LCD SOURCED FROM TAIWAN WHERE ELSE USED: ALESIS MODEL #D4 DRUM MODULE	1	EA
70A071	PWR SUPP 10VAC WALL ADAPT W/ 2.5MM PLUG	1	EA
	ASSY WORDCLOCK MODULE (IN METAL CAN, EPOXY POTTED)	1	EA

PARTS OMITTED:

NOTE: MOTHERBOARD / CIRCUIT DESIGN REVISIONS MAY BE REASON FOR ABSENCE

U1	IC 14 PIN DIP + SM SOCKET
U3	IC 14 PIN DIP + SM SOCKET
U4	IC 14 PIN DIP + SM SOCKET
U5	IC 14 PIN DIP + SM SOCKET
U20	IC 14 PIN SOIC
U27	IC 14 PIN SOIC
U28	IC 16 PIN SOIC
U35	IC 44 PIN PLCC
U36	IC 16 PIN SOIC
U37	IC 16 PIN WIDE SOIC
U39	IC 8 PIN DIP + SM SOCKET
SPR1	IC 20 PIN DIP
SPR2	IC 20 PIN DIP
SPR3	IC 16 PIN SOIC
SPR4	IC 16 PIN SOIC
J7	CONN 8CND PCB MNT SML DIA RND RT ANGL, FOR MAC COMPUTER
SW2	PB-STYLE SW (18 PIN)

NOTE: MOTHERBOARD PCB HAS DUAL ASSY NUMBERS, THE ONE LISTED AND ACTIVE IS:
 ASSY #70348 ALESIS AI-2 I/F REV 1E, THE OTHER UNUSED (SHARPIE BLACKENED) ONE IS;
 ASSY #70349 P2 I/F REV , OBSERVED IN AI-2 SERIAL #A22722695, W/ MB SERIAL #480144, AND EPROM REV 1.01

OPTIONS POSSIBLE BUT NOT INCLUDED:

Video Sync Generator (VSG) option card (ala Microlynx), ASSY #70C025, which apparently can use the VIDEO SYNC IN jack as an output for the VSG card (see test #7 VSG Interface Test). This feature is also mentioned in the Owner's Manual, Ch.6 (6.5F table), pg. 56, and again on pg. 58 & 59. It will apparently output black burst in the same way as is available for the Microlynx, with the video sync signal locked to whatever the System Ref is.